Prototype Silicon Position-Sensitive Detector Working with Bragg–Brentano Powder Diffractometer

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A prototype 64-channel detector module, comprising a silicon strip detector with strip pitch of 100 \(\mu\)m and 64-channel ASIC RX64, was tested with the X-Pert Philips MPD diffractometer. Basic parameters of the detector module, energy resolution, and detection efficiency, were evaluated as a function of the counting rate. Energy resolution of 1.1 keV FWHM for photon rate up to \(1 \times 10^7\) photon/s per 1 cm of the active width of the detector was demonstrated. The prototype detector, when applied in a diffractometer utilizing Bragg–Brentano focusing principle, allows to increase the counting rate by about 2 orders of magnitude with respect to a single counter. Exemplary diffraction patterns of polycrystalline samples of Si and SiO\textsubscript{2} (quartz peak cluster) are presented.

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1. Introduction

Silicon strip detectors are widely used for position-sensitive detection of charged particles in high energy particle physics experiments. Since a decade they are applied for position-sensitive detection of X-ray photons and represent a new class of one-dimensional position-sensitive single-photon counting devices. A strip detector is an array of \(p-n\) reverse biased diodes shaped as narrow strips and made on common high-resistivity \(n\)-type silicon substrate. Such detectors require
each strip to be read out by an individual electronic channel which is possible employing the technique of application specific integrated circuits (ASICs).

This paper describes application of a prototype 64-channel silicon strip detector to a Bragg-Brentano diffractometer, i.e., the most common type of X-ray diffraction apparatus. We demonstrate that in applications to such diffractometer, silicon strip detectors have several advantages not only when compared to the classical arrangement with a single counter but also with respect to other types of position-sensitive detectors.

2. Silicon position-sensitive detector

The prototype 64-channel detector module described in this work comprises two monolithic silicon structures: a strip detector with strip pitch of 100 μm and a 64-channel readout ASIC RX64 [1]. The two chips are assembled on a small printed circuit board and connected together with wire bonding.

The 64-channel readout ASIC RX-64 [1] is the latest prototype being designed and fabricated in the frame of an ongoing program on development of read-out electronics for silicon strip detectors for X-ray detection. Main steps, compared to the previous 32-channel prototypes [2, 3], is integration of the analogue front-end circuitry and the digital part, comprising counters and readout logic, in a single chip and extension up to 64 channels. This integration has improved significantly the robustness of the design and allowed for implementing some new features like internal calibration circuitry and internal digital-to-analogue converter (DAC) to control the threshold of the amplitude discriminators in the front-end circuits from an external computer. Thus, the detector module requires only a digital I/O PC card and the standard power supplies for standard data taking as well as for detailed measurements of performance.

The parameters of the detector module are summarised in Table. One parameter, namely the threshold uniformity deserves an additional comment. A key point in the architecture of the RX64 design is a common discrimination threshold applied to all channels. This solution works providing the channel-to-channel spread of the discriminator offsets is lower compared to the noise so that the effective threshold for each individual channel is not affected by the offset. Since the noise and the threshold spread are both random values and they add in square, the threshold spread (being a half of the noise) is practically negligible. The energy resolution given in Table takes into account both, the resolution of individual channel and the spread of channel parameters.

Basic parameters of the detector module, energy resolution and detection efficiency, was evaluated as a function of the counting rate. Energy resolution of 1.1 keV FWHM for photon rate up to $1 \times 10^7$ photon/s per 1 cm of the active width of the detector was demonstrated. They were determined experimentally with the use of the radiation from X-ray tube (from $\theta - 2\theta$ diffractometer) and
Basic parameters of the detector module.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of channels</td>
<td>64</td>
</tr>
<tr>
<td>Strip pitch</td>
<td>100 µm</td>
</tr>
<tr>
<td>Length of the detector strips</td>
<td>10 mm</td>
</tr>
<tr>
<td>Detector bias voltage</td>
<td>100 V</td>
</tr>
<tr>
<td>Energy resolution</td>
<td>1.1 keV FWHM (for Cu $K_\alpha$)</td>
</tr>
<tr>
<td>Threshold uniformity</td>
<td>0.5 keV FWHM</td>
</tr>
<tr>
<td>Maximum counting rate</td>
<td>200 kHz/channel</td>
</tr>
<tr>
<td>Quantum efficiency</td>
<td>99% for Cu $K_\alpha$</td>
</tr>
<tr>
<td></td>
<td>$\approx$ 50% for Mo $K_\alpha$</td>
</tr>
<tr>
<td>Power consumption</td>
<td>2.5 mW/channel</td>
</tr>
<tr>
<td>Cooling</td>
<td>not required</td>
</tr>
</tbody>
</table>

Fig. 1. Detector efficiency as a function of the counting rate.

suitable absorbers. In the RX-64 chip the shaping time $T_p$ can be tuned in the range between 0.5 µs and 1.0 µs. Using shorter shaping times the maximum counting rate can be increased in expense of somewhat higher noise. Figure 1 shows the measured efficiency as a function of the counting rate for three values of the shaping time. The maximum counting rate given in Table is tentatively defined as corresponding to the number of counts per second at which the efficiency decreases to 70% of the value at low intensity. The loss of efficiency at high counting rates is due to pile-up of pulses in the front-end circuitry and not due to charge collection time in the sensor, which is much shorter than the shaping times in the electronics. This counting rate limit concerns each channel separately and is not dependent on the total detector length, i.e., total number of readout channels. The above value corresponds to 20 MHz/cm, a number which is 2 orders of magnitude higher than for gas position-sensitive detectors (PSDs).
3. Silicon strip detector in $\theta-2\theta$ diffractometer

Projects on application of silicon strip detectors for powder X-ray diffraction described in literature [4, 5] concerned Debye–Scherrer method. In this non-focusing geometry the diffracted photons are to be gathered in angular range close to 180°. Hence, the planned systems consist of thousand channels. We have developed the new application for the diffractometer based on Bragg–Brentano focusing principle [6, 3].

Application of gas PSDs to Bragg–Brentano powder diffractometer were described by Göbel [7] and Cheary and Coelho [8]. The gaseous PSDs (with a central wire electrode, typical length of 50 mm) are optionally offered by manufacturers of the diffractometers [9]. The use of silicon strip PSD offers several advantages over the gas PSD, for example:

— **better spatial resolution** exclusively determined by the strip geometry. Our experiments [2, 3] have shown that the resolution of the diffraction pattern taken with the detector with strip pitch of 100 $\mu$m is the same as for single-counter diffractometer with the receiving slit of 100 $\mu$m;

— **better detection efficiency**, specially evident for Mo (17 keV) radiation;

— **high counting rate** per channel which leads to the values of counting rate per unit length which are two orders of magnitude higher than for the gas PSD.

The last advantage assures that the detector does not saturate in any diffraction experiment with conventional X-ray source. This makes possible to decrease the measurement's time of the typical polycrystalline samples from hours to minutes. The gas PSDs were practically used to speed up the prolonged measurements with the low intensity of diffracted beam (small samples, thin films).

The PSD is located on the detector arm of the diffractometer with the centre coinciding with the focusing circle. Registration of the diffraction beam at the finite distance $e$ from the focusing point gives rise to the distortion of the peak profile.

![Geometry of $\theta-2\theta$ diffractometer working with the linear position-sensitive detector.](image)
(See Fig. 2; note that for $\epsilon \neq 0$ the instrument setting angle $2\phi$ differs with respect to the diffraction angle $2\theta$.) The theory of geometrical equatorial aberration [10] makes it possible to predict both the peak profile and parameters describing that aberration. The contribution to a peak broadening is given (in term of rms peak width) by $\frac{1}{\sqrt{2}} \frac{4A}{R} \cos \phi$, where $R$ is the instrument radius and $2A$ the illuminated length of the sample. Numerical calculation shows that for the detector already used the peak broadening remains negligible, but should be taken into account for future larger systems and small diffraction angles.

In present experiments the background level is comparable to that of traditional diffractometer without crystal monochromator. The background can be reduced by the combination of the beta filter, the use of antiscatter slits and the electronic two-level discrimination. The experiment described in Ref. [2] shows that the use of silicon strip detector with two-level discrimination allows to obtain the background level of the diffraction pattern slightly lower than that for the classical powder diffractometer with beta filter.

## 4. Results

The prototype detector was mounted on the detector arm of an X-Pert Phillips MPD diffractometer. With the instrument radius of 230 mm the detector covers angular range of 1.6°.

In order to obtain a diffraction pattern in large angular range one has to work with “step by step” mode scan. Figure 3 shows such a result for Si standard sample. With 40 steps the total measuring time was 10 min.

Observation of the structure change can be made with the fixed position of the PSD. As an example Fig. 4 shows the diffraction pattern for the so-called “quartz peak cluster”. This feature results from the combination of 122, 203, and 301 Bragg peaks of $\text{SiO}_2$ with $K_{\alpha_1} - K_{\alpha_2}$ splitting and is considered as a standard for comparison of spatial resolution. Measuring time was 3 min.

## 5. Outlook

In the next step we plan to use two 64-channel RX-64 chips for readout of 128 strips simultaneously so the device will be 12.8 mm wide. We consider this size as minimal, which may practically compete in powder diffractometers with existing gas position-sensitive detectors. The 12.8 mm detector with a suitable data acquisition system will be a complete unit for diffractometry. Thanks to high efficiency it will be useful for measurements of thin films and multilayer structures where volume of samples is very small (grazing incidence diffraction (GID) and total reflectivity measurements).

Further development of the front-end electronics includes introducing two-level discrimination and increase in the counting rate. High counting rate capa-
Fig. 3. Diffraction pattern of Si standard polycrystalline sample.

Fig. 4. Diffraction pattern of the “quartz peak cluster”. The scale from 1 to 64 channels corresponds to the interval of 2θ from 67.2° to 68.8°.

bility, which can be increased by proper tuning of the front-end design, makes the device attractive for applications in experiments using synchrotron radiation.

References