Transport Characteristics of Gated Core-Multishell Nanowires: Self-Consistent Approach

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The influence of the applied gate voltage on the coherent propagation of the conduction electrons through the InGaAs/InP core-multishell nanowires with the surrounding gate is considered. The solution of the three-dimensional Schrödinger equation within the effective mass approximation is found using the adiabatic method. The electrostatic potential distribution generated by the all-around gate is determined from the self-consistent procedure applied to the Schrödinger–Poisson problem. The Landauer–Büttiker formalism and quantum transmission boundary method are applied to calculate the transport properties of the considered nanosystem.

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1. Introduction

Recent development of nanotechnology enables to fabricate the semiconductor nanowires (NW) with the core-multishell structure (CMS) for which the outer diameter is less than 30 nm [1]. Such nanostructures find an application in the quantum electronics because of their transport properties which are determined by the quantum size effect and interference phenomena [2] and can be controlled by an external electric field generated by the electrostatic gates. This allows to construct the vertical transistor with all-around gate on the basis of the considered nanostructures [3]. However, the properties of such nanodevices strongly depend on materials and the geometric parameters, as we have shown in our previous report where we have investigated the influence of the gate voltage on the coherent propagation of the conduction electrons through the CMS–NW [4].

In the present report we generalize our previous results for a model including the self-consistent electrostatic potential distribution. For this purpose, we solve the three-dimensional Schrödinger–Poisson problem and we determine the current–voltage characteristics of the CMS–NW via the Landauer–Büttiker theory. In contrast to the previous report [4], the radius of the core and thicknesses of shells are much smaller, which follows recent examples of experimental research [3].

2. Method of calculations

In the present study of a semiconductor CMS–NW we apply a theoretical model given in detail in our previous report [4], but with modified geometric parameters. The core and the outer shell of the nanowire are made of InGaAs, with the InP inner shell placed between them. The nanostucture is surrounded by an all-around gate placed close to the drain electrode. The HfO$_{2.8}$Al$_{0.2}$O insulator layer separates the gate and the wire.

The adiabatic approximation [5] is used to solve the 3D Schrödinger–Poisson problem. It is defined by the system of equations, which consists of the Schrödinger equation

$$
\left( -\frac{\hbar^2}{2m^*} \nabla^2 + U(r) \right) \psi(r) = E\psi(r),
$$

and the Poisson equation with the Dirichlet boundary condition for the electrostatic potential,

$$
\epsilon_0 \nabla \cdot (\epsilon(r) \nabla U(r)) = -\rho(r),
$$

where $m^*$ is the effective mass of conduction electrons, $\epsilon(r)$ is the electrostatic potential and $\epsilon(r)$ is the position-dependent relative electric permittivity. The total potential energy of the conduction electrons consists of three terms

$$
U(r) = U_C(r) + U_B(r) + U_e(r),
$$

where $U_C(r)$ is the hard-wall confinement energy, $U_B(r)$ represents the profile of the conduction band bottom characterizing each layer of the nanowire, and $U_e(r) = -eV(r)$ is the potential energy resulting from the electron–electron interaction and the applied voltages. The charge density is given by the formula

$$
\rho(r) = -e(n(r) - N_D(r)),
$$

where the carrier density $n(r)$ is calculated from the relation

$$
n(r) = \sum_n \int_{-\infty}^{\infty} dE D(E) (f_{FD}(E, \mu_s, T) |\psi_n^s(r; E)|^2 + f_{FD}(E, \mu_d, T) |\psi_n^d(r; E)|^2),
$$

where

$$
f_{FD}(E, E_r, T) = [1 + \exp ((E - E_r) / (k_B T))]^{-1}
$$

is the Fermi–Dirac distribution at temperature $T$ with the electrochemical potential $\mu_s$ of the source, $\mu_d = \mu_s - eV_{ds}$ of the drain, and $\psi_n^s/d(r; E)$ denote wave functions of electrons entering the device from source/drain electrodes, respectively. The total density of states $D(E)$ has the form

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\[ D(E) = \frac{\sqrt{E}}{(2\pi)^2} \left( \frac{2m^*}{\hbar} \right)^{3/2}, \]  
(7)
and the donors distribution \( N_D(r) \) is given by the formula
\[ N_D(r) = \begin{cases} N_D, & \text{source and drain electrodes,} \\ 0, & \text{elsewhere}, \end{cases} \]  
(8)
with \( N_D \) chosen to guarantee the charge neutrality of the device.

Self-consistent calculations used to determine the potential profile are based on the underrelaxation procedure
\[ V_{n+1}(r) = (1 - \omega) V_n(r) + \omega V_{n+1}^s(r), \]  
(9)
where the parameter \( \omega \) has a value between 0.1 and 0.5 chosen to obtain the best possible performance, and \( V_{n+1}(r) \) is obtained directly from the Poisson equation. Convergence of solution is assured by the condition \( \Delta < \epsilon \) where \( \Delta \) is the relative sum of square differences of the potential profiles between last two iterations,
\[ \Delta = \frac{\sum_V (V_{n+1}(r) - V_n(r))^2}{\sum_V V_{n+1}^2(r)}, \]  
(10)
and \( \epsilon \) is the tolerance set to \( 10^{-5} \).

Calculations of every iteration are performed on a uniform computational grid in the following steps: (1) the Poisson equation is solved for the initial density of charge (which is set to zero), (2) the Schrödinger equation is solved for perpendicular 2D cross-sections of the wire, and (3) the wave functions along the \( z \)-axis are calculated for every energy in the considered range. These wave functions are used to determine the transmission coefficient which will be used for electric current calculations. They are also used to reconstruct the 3D wave functions required to find the density of charge which will be used in the next iteration of the self-consistent algorithm.

The analysis of the transport properties of the CMS nanowire is based on the electronic current formula [2]:
\[ I_d(V_{ds}, V_g, T) = \frac{2e}{h} \sum \int_{-\infty}^{\infty} dE \mathcal{T}_n(E; V_{ds}, V_g) \]
\[ \times \left( \mathcal{F}_D(E, \mu_s, T) - \mathcal{F}_D(E, \mu_d, T) \right), \]  
(11)
where \( \mathcal{T}_n(E; V_{ds}, V_g) \) is the transmission coefficient calculated from wave functions obtained from quantum transmission boundary method [6].

### 3. Results and discussion

We have used the parameters from Table I to obtain all the results presented below. For such choice of geometry and other parameters, the potential energy profile resulting from the self-consistent calculations was found in the form shown in Fig. 1 (for comparison, it also shows with dashed line the potential energy profile used in the calculations which do not include the self-consistent procedure). It demonstrates that the influence of the self-consistent procedure is observed mainly within the core of the NW, which is a consequence of the largest charge density in this region.

![Fig. 1. The potential energy profile along the diameter of the nanowire in the middle of the gate (at \( z = 1200 \text{ nm} \)), for \( V_g = 0.2 \text{ V} \) (dark, violet lines) and \( V_g = 0 \text{ V} \) (light, green lines). Dashed line shows the potential energy profile used in the calculations without the self-consistent procedure and the inset presents the only \( U_e \) term of the potential energy.](image_url)

The self-consistent solution of the Poisson–Schrödinger problem, which includes electric charge accumulated in the nanowire, leads to the current–voltage characteristics which is not symmetrical any more, as presented in Fig. 2.

Our calculations prove that for the negative values of \( V_{ds} \), electric charge gathered in the potential energy well created by the gate near the drain electrode reduces the depth of the well. Therefore, it changes overall energy profile of the device, in a way which reduces the transport of electrons from the source to the drain. As a result of this, the absolute value of the negative electronic current increases. On the other hand, for the positive voltage \( V_{ds} \), electrons gathered near the source electrode generate potential barrier which partially blocks transmission and reduces the output current.

Additionally, we have checked that for \( V_g = 0 \text{ V} \) the solution is symmetric. It stems from the fact that the

<table>
<thead>
<tr>
<th>Wire length (nm)</th>
<th>Gate length (nm)</th>
<th>Gate–drain distance (nm)</th>
<th>Source/drain electrode length (nm)</th>
<th>Core radius (nm)</th>
<th>Insulating shell thickness (nm)</th>
<th>Oxide thickness (nm)</th>
<th>Oxide relative permittivity</th>
<th>Core relative permittivity</th>
<th>Shell relative permittivity</th>
<th>Temperature (K)</th>
<th>Fermi energy (meV)</th>
<th>Effective mass (m)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1200</td>
<td>200</td>
<td>50</td>
<td>100</td>
<td>11</td>
<td>4</td>
<td>20</td>
<td>120</td>
<td>13</td>
<td>10</td>
<td>4</td>
<td>100</td>
<td>0.04113</td>
</tr>
</tbody>
</table>

Table I Geometrical and material parameters of the single all-around gated core-shell nanowire.
nanosystem contains no potential energy well in which electrons could concentrate. Simultaneously, the potential energy barriers generated by the electric charge gathered near the electrode of higher energy (from which the current flows) are also higher. In consequence, the electronic current is reduced when compared to the case with neglected accumulation of the electrons.

4. Conclusions

We have performed calculations of the current–voltage characteristics for the semiconductor nanowires with the core–multishell structure, applying the self-consistent procedure to determine the potential energy profile. The presented results indicate that the energy profile is strongly modified by the electric charge accumulated in the core region of the nanowire. In turn, it significantly affects the transmission coefficient and thereby we observe reduction of the electronic current through the considered nanosystem. The contribution of the accumulated electrons to the potential energy can either increase or decrease electric current, depending on the drain–source voltage. It is also affected by the position of the gate which is located asymmetrically in the proximity of the drain electrode in the considered case.

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References