Proceedings of the 43rd "Jaszowiec", International School and Conference on the Physics of Semiconductors, Wisła 2014

# Sb Layers on *p*-GaN: UPS, XPS and LEED Study

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The electronic structure of p-type GaN(0001) surfaces and its modification by antimony adsorption, and properties of Sb/GaN(0001) interface, are presented in this report. The studies were carried out *in situ* by ultraviolet photoelectron spectroscopy, X-ray photoelectron spectroscopy, and low-energy electron diffraction. Thin Sb layers were deposited under ultrahigh vacuum conditions onto the substrate at room temperature. Electron affinity of clean p-GaN surface amounted to 3.0 eV. A small amount of Sb on GaN(0001) surface reduced the electron affinity to 1.9 eV. The work function of the Sb layer was equal to 4.4 eV. For the Schottky barrier height of the Sb/GaN interface, the value of 2.50 eV was obtained.

DOI: 10.12693/APhysPolA.126.1128

PACS: 81.05.Dz, 68.37.Ps, 68.37.Ef, 68.37.-d, 81.10.Bk, 81.07.-b

## 1. Introduction

Gallium nitride (GaN), a wide band-gap semiconductor, is very attractive to electronic technology for devices and components like visible and UV lasers, light-emitting diodes, high temperature and frequency detectors, and transistors [1]. Antimony is an important element for electronics and optoelectronics technologies owing to its good thermal conductivity and a high oxidation- and corrosion resistance. It is also used as surfactant for better control of elementary growth processes responsible for the III–V semiconductor fabrication [2].

In our investigation antimony was used as a material for the interface formation on the GaN substrate and for changing the electronic properties of the semiconductor. Results of the Sb/GaN interface formation after Sb deposition from vapor and morphological changes at the GaN surface caused by post-deposition annealing are described. We found that a small amount of Sb, which remained on GaN surface after the annealing process, led to a substantial lowering of the electron affinity of the semiconductor's surface. The observed effect can be very helpful in producing the ohmic contact or heterojunction on the p-GaN substrate.

## 2. Experimental details

Samples, around  $8 \times 4 \text{ mm}^2$  in size, were cut out from a GaN(0001)/Al<sub>2</sub>O<sub>3</sub> wafer (TDI Inc.) and mounted on molybdenum plates. They consisted of atomically flat, (0001)-oriented, *p*-type, Mg-doped ( $N_a = 10^{18} \text{ cm}^{-3}$ ), and 10  $\mu$ m thick epitaxial GaN layers grown on a polished Al<sub>2</sub>O<sub>3</sub>(0001) template. The substrates, Sb/GaN interfaces and the Sb films were characterized by X-ray photoelectron spectroscopy (XPS) using an Mg  $K_{\alpha}$  (1253.6 eV) radiation source, and by ultraviolet photoelectron spectroscopy (UPS) using the He(I) line (21.2 eV) from a DC

discharged lamp; additional analyses were carried out by low energy electron diffraction (LEED). These techniques were housed in an ultrahigh vacuum (UHV) chamber with the base pressure  $\leq 1 \times 10^{-10}$  Torr. Photoelectrons were collected with a hemispherical electron energy analyzer (Phoibos 100, Specs) with 0.1 eV steps. Optical axis of the analyzer entrance was normal to the substrate surface. The position of the Fermi level  $(E_{\rm F})$  was measured on a clean Au sample. The XPS and UPS spectra were analyzed using the Casa XPS software. The presence of carbon and oxygen was monitored using the AES C(KLL) O(KLL) lines. Before measurements, the GaN samples were *ex situ* degreased in alcohol, then washed in distilled water and dried in air, next the substrate surface was cleaned in situ by cycles of annealing at 800 °C in order to remove surface oxides and carbon contamination. This was done by electron bombardment of the substrate from the backside. The Sb (99.999% purity) was evaporated from an electron beam evaporator under an operating pressure lower than  $1 \times 10^{-9}$  Torr. Ion current of the Sb molecular beam was used to control the efficiency of evaporation. Average coverage of Sb layers was quantified by the XPS method [3], the one-monolayer thickness was taken as equal to the diameter of an Sb atom. During experiments the residual gas pressure in the chamber was controlled by a quadrupole mass spectrometer. All the measurements were done at room temperature (RT).

#### 3. Results and discussion

After in situ annealing the substrate several times at  $800 \,^{\circ}$ C, the Ga-rich surface of GaN(0001) with a very small amount of residual oxygen was obtained. The AES C(KKL) line was no longer present in the wide spectrum. The Ga 3d line at 19.5 eV had the full width at half-maximum (FWHM) equal to 2.1 eV. Its deconvolution into three components gave attributes of Ga-N and Ga-O bonds, and metallic Ga. The UPS spectrum of valence band is typical of a semiconductor (see Fig. 1, curve 1). Position of the binding energy at valence band maximum (VBM) is situated at 1.5 eV

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below the  $E_{\rm F}$ , as determined from UPS measurements by extrapolation of the line fit to the leading edge of the spectrum. The VBM lies at 18 eV above the Ga 3*d* core level, which is in good agreement with the value reported by Bermudez [4]. The electron affinity of the GaN(0001) surface amounts to 3.0 eV, as calculated from the relationship  $\chi = hv - W - E_{\rm g}$ , where hv = 21.2 eV is the energy of photons, the W = 14.8 eV is the width of recorded spectrum measured as the energy difference between the VBM and the cut-off threshold of the spectrum, and the  $E_{\rm g} = 3.4$  eV is the band gap of the semiconductor.



Fig. 1. UPS valence band spectra for (1) the clean p-GaN, (2) 5 nm thick Sb layer deposited on the GaN(0001), (3) Antimonized p-GaN(0001) surface with about 0.2 monolayer Sb obtained after annealing (2) at 400 °C.

LEED patterns, with sharp diffraction spots and a dark background, revealed a highly ordered surface of the  $(1 \times 1)$  structure. Instead of normal order spots, a sextet of diffraction spots appeared in the hexagonal configuration, such as in our previous work [5]. XPS measurement showed that, after deposition of an Sb layer, the Ga 3d line shifted toward the high binding energy to reach 20.5 eV and diminished with increase of evaporation time, while the signal from the Sb 4d peak at 30 eV increased. The Schottky barrier height (SBH) at the Sb/GaN interface was determined from XPS [6, 7]. The SBH for the *p*-type semiconductor can be calculated from the formula  $\varphi_{\rm B} = E_{{\rm Ga}3d}^m - \Delta E$ , where the  $E_{{\rm Ga}3d}^m$  is the position of the Ga 3*d* core level line as it is shifted after Sb deposition. The  $\Delta E = 18$  eV defines the initial binding energy of the Ga 3d core level with respect to the VBM as mentioned above. SBH of the Sb/GaN interface, as calculated basing on these data, amounts to 2.50 eV.

For the relatively thick Sb layers (of about 5 nm), where spectral lines of substrate have almost disappeared, the band reveals a metallic character (see Fig. 1, curve 2). A high density of states occurs in the range between the  $E_{\rm F}$  and the VBM. The work function of the Sb layers was calculated from the expression  $\varphi_{\rm m} = hv - W$ , where W was estimated to be 16.8 eV. The resulting value was 4.4 eV. This value is consistent with literature data, where it ranges between 4.0 and 4.56 eV [8, 9]. No diffraction pattern was observed for Sb layers. This indicates that the antimony layers had a grainy structure beginning from the earliest stages of growth.

The effect of the presence of Sb atoms on the electronic properties of the substrate was investigated. To achieve the objective the interface was short annealed at  $400\,^{\circ}\text{C}$ which led to incorporation of a small amount of Sb into the GaN surface. The UPS spectra indicate recovery of the valence band typical of a semiconductor from that of metals (curve 3 in Fig. 1) The position of VBM lies at 1.6 eV below the  $E_{\rm F}$ , and the width W of the recorded spectrum is 15.9 eV. Based on these values the electron affinity of the antimonized GaN(0001) surface was obtained to be 1.9 eV. The XPS spectrum of Ga 3d (see Fig. 2) is shifted by 0.6 eV from the position 20.5 eV toward the low binding energy with its FWHM at 2.1 eV; the position of Sb 4d remains unchanged but the FWHM is slightly increased from 2.3 to 2.4 eV. LEED patterns were the same as for the clean substrate, except that the diffraction spots for the low electron energy were slightly less sharp.



Fig. 2. The Sb 4d and Ga 3d XPS spectra from Sb/GaN(0001) interfaces where: (1) thickness of Sb layer was 5 nm, (2) thickness of Sb layer was 0.2 monolayer obtained after annealing (1) at 400 °C.

We elaborated the energy diagram of the p-GaN shown in Fig. 3. For this purpose we determined the position of the  $E_{\rm F}$  relative to the VBM in bulk GaN. We have done the calculation for RT by data available in [10]. The result of 0.1 eV indicates that the Fermi level is only 0.1 eV above the VBM in the bulk. We conclude that the downward band bending is 1.4 eV at the clean surface of the semiconductor. Let us note the slight shift of the VBM between the clean p-GaN(0001) surface and the surface of the substrate with small amounts of Sb. The difference in VBM value amounts to 0.1 eV. The vacuum level is lowered by 1.2 eV. The presence of antimony on the surface leads to the lowering of the electron affinity from 3.0 to 1.9 eV.



Fig. 3. Schematic energy diagram of the p-GaN(0001) surface showing the Sb-induced lowering effect of electron affinity.

#### 4. Conclusions

Combined surface-analysis techniques of UPS, XPS and LEED were used to investigate chemical and electrical properties of the Sb/GaN(0001) interface. The electron affinity is 3.0 eV and the downward band bending is 1.4 eV at the clean surface of p-GaN(0001). Diffraction pattern exhibits a highly ordered surface of the  $(1 \times 1)$ structure. Incorporation of a small amount of Sb into the p-GaN(0001) surface leads to a substantial decrease of the electron affinity to 1.9 eV. After the procedure LEED patterns remain the same like those for the clean surface of the semiconductor. The work function of the Sb layers equal to 4.4 eV has been calculated from the obtained results. The XPS measurements have excluded a chemical interaction at the Sb/GaN interface for the RT deposited Sb layers. The Schottky barrier height of the Sb/GaN interface, as calculated from the data acquired by using XPS and UPS, is equal to 250 eV.

## Acknowledgments

The authors would like to thank Dr. Stanisław Surma for helpful discussions and his assistance during preparation of manuscript. The work was supported by the Wrocław Research Centre EIT+ within the project "The Application of Nanotechnology in Advanced Materials" – NanoMat (POIG.01.01.02-02-002/08) co-financed by the European Regional Development Fund (Operational Programme Innovative Economy, 1.1.2).

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