

Measurement of Pulsed Current–Voltage Characteristics of AlGa_N/Ga_N HEMTs from Room Temperature to 15 K

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We report measurements of the pulsed and dc current–voltage characteristics of AlGa_N/Ga_N high-electron-mobility transistors as functions of geometry, temperature (from 300 down to 15 K), and operating conditions. An increase in the drain current with shortening of the pulse width from 1 μs to 400 ns is found to be significant at room temperature whilst this behavior is inverted or even removed at 77 and 15 K temperatures.

PACS: 72.80.Ey, 73.21.Fg, 73.40.–c

1. Introduction

In recent years, the AlGa_N/Ga_N HEMTs have become promising candidates because of their applications as high power and high temperature microwave devices [1]. In particular, the AlGa_N/Ga_N HEMTs show high transconductance, high cut-off frequencies and good thermal management [2]. For example, the dispersion effects [3], the kink effect at 300 and 100 K temperatures [4], the self-heating effects [5], are among recent reports in the literature [6]. However, few investigations exist in the literature dealing with pulsed measurements especially at low temperature. In this letter, we report the pulsed and continuous I – V characteristics of AlGa_N/Ga_N HEMTs as functions of geometry, temperatures (from 300 down to 15 K) and operating conditions.

2. Experimental setup

The experimental setup used for the dc I – V measurements consists of a dual-channel system source-meter, and the device under test (DUT) installed inside a cryostat for varying the temperature from 300 to 77 and 15 K. The voltage sweep is performed for 5 ms between two voltage points of the dc I – V curves. The experimental setup shown in Fig. 1 is used for the pulsed I – V measurements where the source-meter is replaced by a function

generator producing a voltage square pulse (V_{applied} in the figure) with a repetition rate of 100 μs. An oscilloscope is introduced to track the temporal profile of short (400 ns) and long (1 μs) pulses. The drain current is calculated from the voltage drop across the 50 Ω resistance of the function generator.

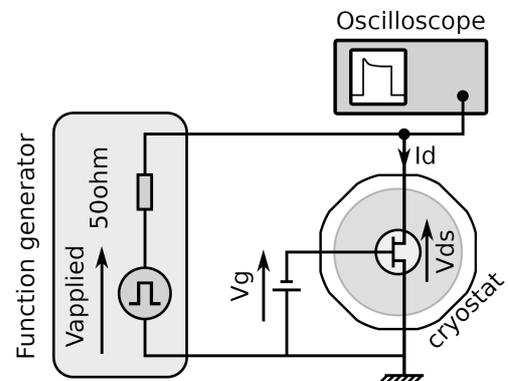


Fig. 1. Block diagram of the experimental setup used for pulsed I – V measurements at 300, 77 and 15 K temperatures.

The sample is cooled down at 77 K by using a flow of liquid nitrogen, then cooled down to 15 K by using a helium compressor.

As far as the fabrication process of the Ga_N HEMTs is concerned, the different layers consist of a sapphire substrate, a Ga_N iron doped template, a Ga_N 1 μm thick buffer, a 1 nm AlN/21 nm Al_{0.28}Ga_{0.72}N/3 nm Ga_N ac-

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tive layer covered with Ti/Al for ohmic and Ni/Au for the Schottky contacts. The layers are grown and processed as described in [7]. The devices are not passivated. An ohmic contact resistance from 1 to 3 Ω mm is obtained depending on the sample. Capacitance voltage and the Hall measurements attest the presence of a two-dimensional electron gas with a carrier concentration of 10^{13} cm^{-2} and a mobility of $2000 \text{ cm}^2/(\text{Vs})$.

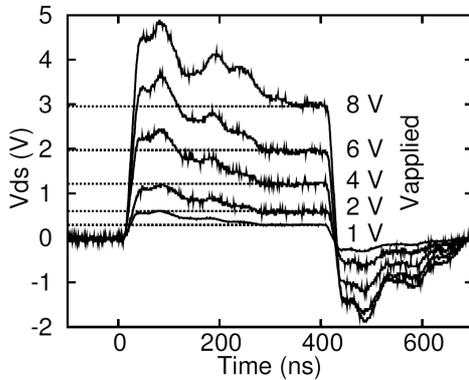


Fig. 2. Temporal evolution of drain–source voltage for varying the voltage from 1 V to 8 V at a fixed gate voltage of -1 V.

The temporal profiles of the pulses are found to be fairly stable during the measurements. Nonetheless, the situation shown by Fig. 2, for a short pulse (400 ns) at low temperature (15 K), is the worst and limiting case in terms of stability and accuracy of the recorded pulses. The shape of the pulse is stable and nearly a square-pulse for 1 V whilst the shape is considerably altered on increasing the drain voltage from 1 to 8 V. The transient behavior at the beginning of the pulses may arise from the impedance of wires, connections, parasitic reactance and their resonant response to the pulses excitation. Since the voltage is stable in the final part of the pulses, the device voltage and current values are extracted by performing a time average of this portion.

3. Results and discussion

The I – V characteristics of the GaN HEMTs having geometry, $L_g = 3 \mu\text{m}$, $L_{ds} = 15 \mu\text{m}$, are shown in Fig. 3a for an applied gate voltage of -3 V. We start the discussion of the experimental results by analyzing the HEMT behavior at room temperature. The lower values of the drain current in dc regime is attributed to the self-heating effect that is consistent with recent reports [3, 4]. The dc curve shows relatively lower values of the drain current I_d while its value is increased on shortening the pulse-widths from $1 \mu\text{s}$ down to 400 ns. The pulsed I – V current values overpass the dc ones because the heating effects are reduced in the case of the pulsed measurements, and hence the drain current is enhanced. On the other hand, the heating effect dominates in the case of the dc

I – V measurements thus reducing the current. A similar kind of enhancement in the drain current on shortening the pulse width is obtained for longer geometries ($L_g = 4 \mu\text{m}$, $L_{ds} = 16 \mu\text{m}$) (Fig. 3b) except for the fact that the difference of the drain current values is sharply reduced for $1 \mu\text{s}$ and 400 ns pulse voltages.

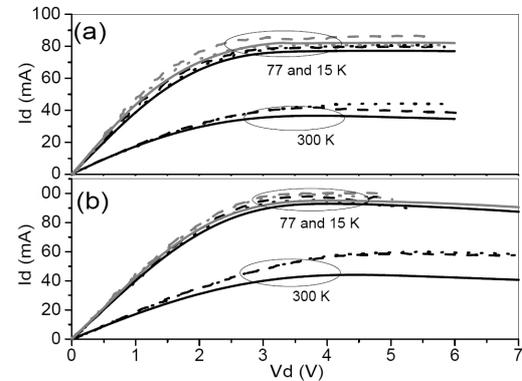


Fig. 3. Current–voltage characteristics of the GaN HEMTs with geometries of $L_g = 3 \mu\text{m}$ and $L_{ds} = 15 \mu\text{m}$ (a) with a gate voltage $V_g = -3$ V, and $L_g = 4 \mu\text{m}$ and $L_{ds} = 16 \mu\text{m}$ (b) with a gate voltage $V_g = -1$ V at 300 (black curves), 77 (black curves) and 15 K (gray curves) temperatures. The continuous measurements are shown by solid line curves whilst the pulsed measurements, with varying pulse widths of $1 \mu\text{s}$ and 400 ns, are indicated by dashed and dotted line curves, respectively.

Next, we investigate the dc and pulsed I – V characteristics of the device under study at low temperatures. In the case of relatively small geometries, the drain current is enhanced by a considerable value on cooling the sample down to 77 K. This means that the dynamics of the charge carriers is significantly affected by the cooling effect between 300 and 77 K due to the reduced phonon interactions. Between 77 and 15 K, the drain current has practically attained its maximum value and only a small increase is measured (refer to topmost parts of Fig. 3a). The effect of changing the pulse width on the drain current at 77 and 15 K is removed and inverted, respectively, compared to 300 K. For smaller geometries at 77 K, the drain current remains the same for $1 \mu\text{s}$ and 400 ns pulse voltages but the I_d still has lower value. A similar kind of behavior is also seen at 15 K except for the fact that the drain current has relatively higher values at 15 K compared to 77 K. In Ref. [3], it was shown that the drain–source current at 100 K for $V_g = 0$ V is almost the same for the dc as well as for the pulsed measurements. The drain current was also found to have a lower value for the pulsed measurements compared to the dc ones for $V_g = -1$ to -3 V. However, in our measurements, the effect of altering the pulse voltages on the drain current is dependent on the geometry. We stress that probably the effect of current enhancement by decreasing the pulse width was not evidenced in the results of Ref. [3] at high V_g due to the fact that the temperature was decreased only up to 100 K. Recalling the fact that the self-heat

could be easily dissipated in the smaller devices, the drain current increases on reducing the pulse voltage from 1 μ s to 400 ns (Fig. 3a). One can also see that the drain current at 15 K is nearly the same for 400 ns and the dc measurements (Fig. 3a) which indicate that the effect of altering the pulse voltage is removed in this case. On the other hand, in the case of relatively longer geometry at 77 K, the drain current has nearly the same values for the dc as well as 1 μ s and 400 ns pulse voltages. There is also a negative slope at higher V_d . However, the value of drain current is lower in the case of 400 ns pulse voltage compared to 1 μ s which indicates that the effect of the variation of the pulse-width voltage is inverted in this case.

It is interesting to note that the above discussed inversion behaviour and even its removal (refer to 400 ns and the dc results) suggests that the traps at 77 and 15 K may play a different role in the motion of the carriers. However, in the driving circuit of Fig. 1, the voltage step appearing between drain and source, V_{ds} , not only affects the drain of the HEMT, but also its gate at the beginning of the pulse. Indeed, it should be pointed out that it is very difficult to get rid of the parasitic reactance present in the circuit due to the wiring of the samples in the cryostat and for this reason, we performed a time average on the end part of the pulses. We repeated these experiments on several samples and found similar results. Hence, we believe that the observed inversion behaviour is due to the variation of the pulse voltages, although it is not possible to get rid of the parasitic effects.

It is also noteworthy that we have not noticed any pronounced *kink* for all studied geometries at both room and cryogenic temperatures. As the presence of kinks is often attributed to impact ionization and trapping effects, this indicates the good quality of the studied sample material. It should be pointed out that the effect is dominant in the case of surface degradation of the sample, and it depends on the sample quality. Consequently, the observation of the kink may be due to the different fabrication processes of HEMTs, and geometries [3] compared to those which we are using.

4. Conclusion

In summary, we have experimentally investigated dc and pulsed current–voltage characteristics of AlGaIn/GaN HEMTs as functions of geometry, temperature, and operating conditions. We emphasize that all reported results have been found to be stable and reproducible in time and on different samples. The increase in the drain current with shortening of the pulse width is found to be significant at room temperature whilst this behavior is not only inverted but also ceased at 77 and 15 K.

Acknowledgments

The French National Research Agency (ANR) and the Lithuanian Science Council are sincerely acknowledged for funding the research work under contract AITHER no. ANR-07-BLAN-0321 and grant no. MIP-87/2010. This work was partially supported by GIS 053713 TeraLab Montpellier.

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