

Influence of SiC Surface Preparation on Homoepitaxial Growth; X-ray Reflectometric Studies

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X-ray reflectometric and diffraction topographic methods were applied for examination of 4H and 6H silicon carbide substrates finished with various regimes, as well as, silicon carbide epitaxial layers. The investigations indicated a very good quality of the substrate surfaces finished with the process established at the Institute of Electronic Materials Technology, which provided the surface roughness $\sigma = 0.55 \pm 0.07$ nm for 4H-SiC wafers. These values were better than those of substrate wafers offered by many commercial producers. The surface roughness was decreased during the initial high temperature etching to $\sigma = 0.22 \pm 0.07$ nm. A relatively good structural quality was confirmed in the case of 4H epitaxial wafers deposited on the substrates prepared from the crystals manufactured at the IEMT, with the 8° off-cut from the main (001) plane.

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1. Introduction

The technology of modern high temperature electronic materials often includes the epitaxial deposition of silicon carbide (SiC) on the SiC substrates. The essential for the applications is achieving a high perfection of the deposited layers, especially the elimination of micropipes and basal plane dislocations. The last ones are very dangerous in high-voltage devices in view of their possible migration associated by the formation of large stacking faults.

Deposition of perfect epitaxial layers is even more dependent on the appropriate finishing of the surface, than on the concentration of defects in the substrate crystals. Nevertheless, the last factor can not be to any extent neglected [1, 2]. Obtaining of the suitably good quality of the surface finishing is otherwise very difficult in the case of silicon carbide, as it is a very hard material and resistant against many of chemical reagents. Several methods of surface finishing were proposed [1–5]. The influence of defects present in the bulk of substrate wafers on the perfection of deposited homoepitaxial layers of silicon carbide have been discussed [6–9]. It should be noted that the quality of silicon carbide epitaxial layer is also strongly affected by the high temperature etching in the mixture of hydrogen and gaseous propane usually applied at the first stages of the epitaxial process.

In the present work, the X-ray reflectometric investigations were performed for a number of samples both of 4H- and 6H-SiC polytypes prepared using finishing

regimes developed at the Institute of Electronic Materials Technology (IEMT). The structural perfection of epitaxial layers and of substrate wafers was also controlled by synchrotron X-ray diffraction topography realized both in white and monochromatic beams at HASYLAB.

2. Experimental

The specular reflectometric investigation were performed by means of adapted a high resolution X-ray diffractometer arrangement [10]. The important point of the present experimental setup was the use of the vertical linear focus from a standard diffractometric X-ray tube. This enabled an effective separation of the characteristic $\text{CuK}\alpha_1$ line by means of a single channel-cut Ge monochromator, and obtaining a high intensity of the probe beam. The experimentally evaluated beam divergence did not exceed 8 angular seconds. As the reflectometric measurements were performed in the vicinity of the critical angle of total reflection θ_c given by the formula

$$\theta_c = (|\chi_0|)^{1/2}, \quad (1)$$

(where χ_0 is zero Fourier component of the dielectric susceptibility), the illuminated surface area was approximately 8×14 mm². The values of the surface roughness were evaluated by fitting of theoretical reflectivity curves to the experimental ones, by means of REFSIM program.

The defect structure of SiC epitaxial layers was investigated by means of synchrotron diffraction topography at the F1 and E2 experimental stations of DORIS III in

HASYLAB. Both the white and monochromatic beams ($\lambda = 0.1115$ nm) topographic methods were used in the Bragg geometry. The section topography (with the application of a fine $5 \mu\text{m}$ slit and the glancing angle of 5°) enabled the indication of volume character of the defect distribution. High sensitivity to strains associated with small inclusions and dislocation loops was provided by monochromatic beam topography.

The silicon carbide epitaxial layers were deposited at the IEMT with the chemical vapor deposition method (CVD) using the epitaxial reactor manufactured by Aixtron [11]. The epitaxial reactor was also used for preparation of preetched substrates by performing only the initial stage of the epitaxial process. Some of the substrates were cut from bulk silicon crystals grown at the IEMT using the physical vapor transport (PVT) method.

3. Results and discussions

The X-ray specular reflectivity measurements were performed with $\theta/2\theta$ scans for the samples of 6H and 4H polytype from SiC substrate wafers cut with 4° off-cut, 8° off-cut and 0° off-cut (i.e. on-axis) with respect to the (001) plane. The samples were prepared as epi-ready substrates by commercial manufacturers SiCrystal and Cree Inc., as well as, by the IEMT. The obtained X-ray reflectivity curves for 4° and 0° off-cut 4H-SiC substrates are presented in Fig. 1. The values of the surface roughness σ evaluated from the best fit of theoretical curves are slightly higher than 2 nm, namely: $\sigma = 2.09 \pm 0.05$ nm for 4H-SiC wafer with 0° off-cut, $\sigma = 2.16 \pm 0.05$ nm for 4H-SiC with 8° off-cut and $\sigma = 2.17 \pm 0.05$ nm for 4H-SiC with 4° off-cut. The evaluated values of the surface roughness for all examined SiC substrates and epitaxial layers are listed in Table.

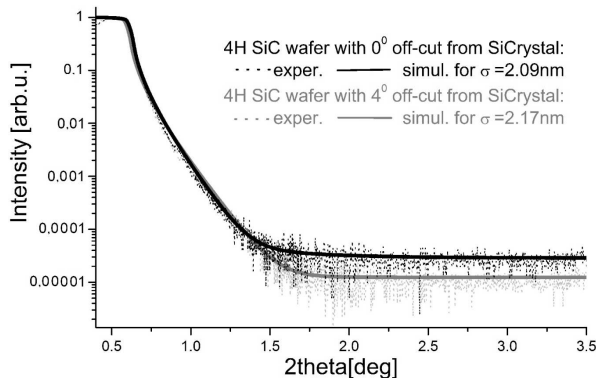


Fig. 1. The measured and simulated X-ray reflectivity curves for substrate wafers manufactured by SiCrystal with: 4° off-cut and the evaluated roughness $\sigma = 2.16 \pm 0.05$ nm, and 0° off-cut (on-axis) and the evaluated roughness $\sigma = 2.09 \pm 0.05$ nm.

It was indicated that in the case of 4H homoepitaxial layers the evaluated values of the layer roughness can be significantly higher than those of substrates. As may

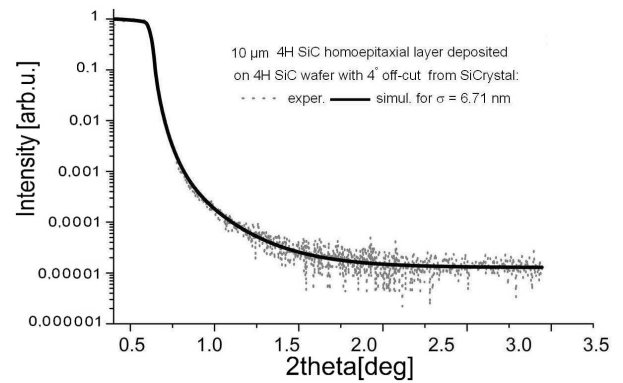


Fig. 2. The measured and simulated X-ray reflectivity curves for the sample consisted of 4H-SiC epitaxial layer with the thickness of $10 \mu\text{m}$, on 4H-SiC wafer with 4° off-cut. The evaluated roughness of the epitaxial layer was $\sigma = 6.71 \pm 0.05$ nm.

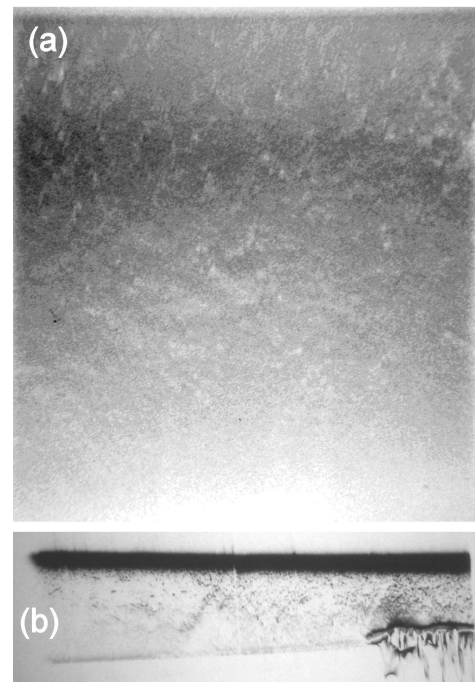


Fig. 3. Synchrotron white beam projection (a) and section (b) topographs of the same (as in Fig. 2) 4H-SiC epitaxial layer grown on 4H-SiC substrate (manufactured by SiCrystal) with 4° off-cut. The grain-like contrast is interpreted as the result of the not perfect finishing of the surface.

be seen in Fig. 2, for the heavily nitrogen doped layer (to 10^{18} cm^{-3}) deposited on the substrate wafer with 4° off-cut manufactured by SiCrystal, the roughness of the deposited layer was $\sigma = 6.71 \pm 0.05$ nm, while the roughness of the substrate was $\sigma = 2.17 \pm 0.05$ nm. For this layer, the synchrotron white beam projection topograph, shown in Fig. 3, has revealed a grain-like contrasts, most probably corresponding to a high concentration of struc-

tural defects. These defects are less visible in the Bragg-case section topograph, shown in Fig. 3b, which revealed many defects (mainly dislocations) present in the substrate. It indicates that commercial substrates often contain some imperfections of surface finishing, causing the generation of defects in deposited epitaxial layers. Fig-

ure 4 illustrates the defects caused by the not perfect surface finishing in 10 μm epitaxial layer on SiC substrate with 8° off-cut manufactured by SiCrystal. Namely, the synchrotron images revealed dislocations with low density (dominantly of screw type) and some defects generated at residual scratches on the surface of the substrate.

TABLE

The evaluated values of the surface roughness of the examined silicon carbide substrate wafers and 4H-SiC homoepitaxial layers.

Sample No.	SiC polytype	Substrate manufacturer	Off-cut [deg]	Special treatment	Evaluated surface roughness σ [nm]	Thickness of the epitaxial layer [μm]
1	4H-SiC	SiCrystal	8		2.16 ± 0.05	
2	4H-SiC	SiCrystal	0		2.09 ± 0.05	
3	4H-SiC	SiCrystal	4		2.17 ± 0.05	
4	4H-SiC	SiCrystal	8	pre-etching I-type	2.19 ± 0.05	
5	4H-SiC	SiCrystal	8	pre-etching II-type	0.22 ± 0.07	
6	4H-SiC	IEMT	8	additional electrochemical polishing	0.55 ± 0.07	
7	6H-SiC	Cree Inc.	0		0.86 ± 0.05	
8	6H-SiC	IEMT	0		1.88 ± 0.05	
9	6H-SiC	IEMT	0		1.78 ± 0.05	
10	6H-SiC	SiCrystal	0		1.21 ± 0.05	
11	6H-SiC	IEMT	4	additional electrochemical polishing	0.85 ± 0.05	
12	4H-SiC	IEMT	8	additional electrochemical polishing pre-etching II-type	1.14 ± 0.05	20
13	4H-SiC	SiCrystal	4	pre-etching I-type	6.71 ± 0.05	10

Our study of the initially pre-etched sample, obtained with the use of epitaxial generator, has revealed a significant difference of the surface quality for various pre-etching regimes, as it is also shown in Table. A properly chosen pre-etching regime improved the substrate surface roughness up to $\sigma = 0.22 \pm 0.07$ nm.

In order to receive a perfect epitaxial layer, an improved finishing process of the substrate surface and the new pre-etching process before the epitaxial process were established at the IEMT. The application of more fine surface lapping decreased the surface roughness to 0.85 nm for 6H-SiC with 4° off-cut, as it is illustrated in Fig. 5. This value was close to the surface roughness of 6H-SiC on-axis substrates manufactured by Cree Inc. Additional finishing with electrochemical treatment

(ECMP) for 4H-SiC sample with 8° off-cut from the IEMT resulted in the surface roughness $\sigma = 0.55$ nm.

The epitaxial layer with a thickness of 20 μm was deposited after pre-etching process on the 4H-SiC substrate wafer with 8° off-cut which was manufactured at the IEMT. The evaluated value of the surface roughness was $\sigma = 1.14 \pm 0.05$ nm. The synchrotron white beam projection (a) and section (b) topographs of this sample are presented in Fig. 6, indicating a relatively good perfection of the layer, as it is shown by individual dislocations and some pipe-formed cavities. However, it should be noted that the PVT generator used at the IEMT provided crystals containing a higher concentration of defects than in the case of crystals grown by SiCrystal and Cree Inc.

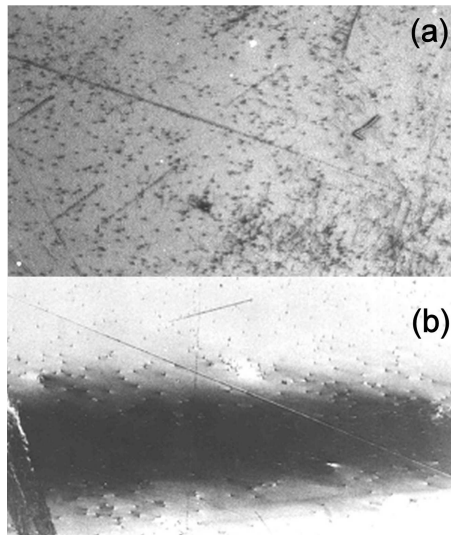


Fig. 4. The back reflection white beam projection synchrotron topograph (a) and the monochromatic beam topograph (b) in (004) plane of 10 μm thick epitaxial layer on 4H-SiC wafer with 8° off-cut manufactured by SiCrystal, revealing dislocations and some defects caused by scratches on the surface of the substrate.

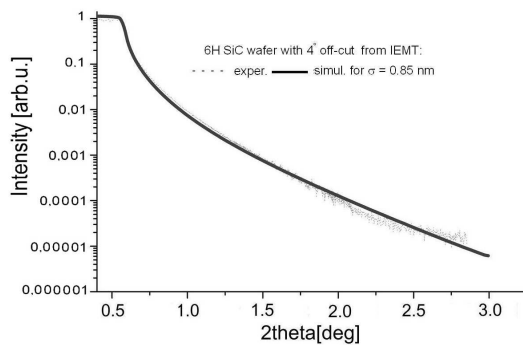


Fig. 5. The measured and simulated X-ray reflectivity curves for 6H-SiC substrate wafer with 4° off-cut manufactured at the IEMT, with the evaluated roughness of the surface $\sigma = 0.85 \pm 0.05$ nm.

4. Conclusions

X-ray reflectometric investigations were performed on numerous samples both of 4H and 6H polytypes prepared by commercial manufacturers SiCrystal and Cree Inc., and at the IEMT. The investigations confirmed a very good quality of the surface finishing with the processes actually developed at the IEMT providing, in case of 4H wafers, the surface roughness $\sigma = 0.55 \pm 0.07$ nm. This value was better than those of substrate wafers offered by many commercial producers. A relatively good structural quality was confirmed in the case of 4H epitaxial layers deposited on wafers prepared from the best crystals with the 8° off-cut from (001) plane, manufactured at the IEMT.

It was indicated that surface finishing was improved

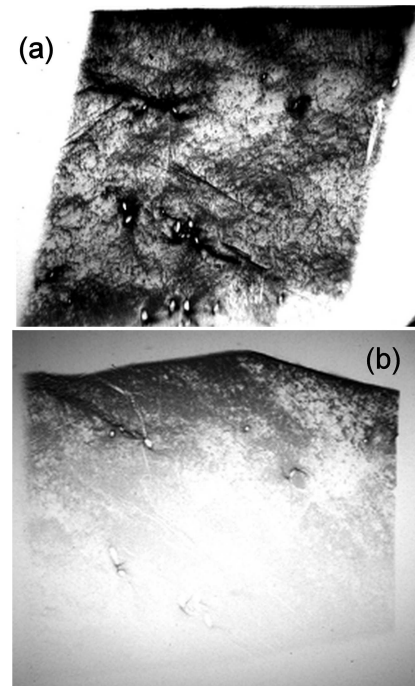


Fig. 6. Synchrotron white beam projection (a) and section (b) topographs of the sample of 4H-SiC polytype with 8° off-cut, finished by means of ECMP, with 20 μm thick SiC epitaxial layer manufactured at the IEMT.

to $\sigma = 0.22 \pm 0.07$ nm by the appropriate initial high temperature etching.

The synchrotron topographic investigation indicated that the not perfect surface finishing led to formation of defects and strains in deposited epitaxial layers. For the sample with 4H-SiC homoepitaxial layer grown on the substrate with 4° off-cut manufactured by SiCrystal we observed a characteristic grain-like contrast and a broadening of the reflection curves.

Acknowledgments

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